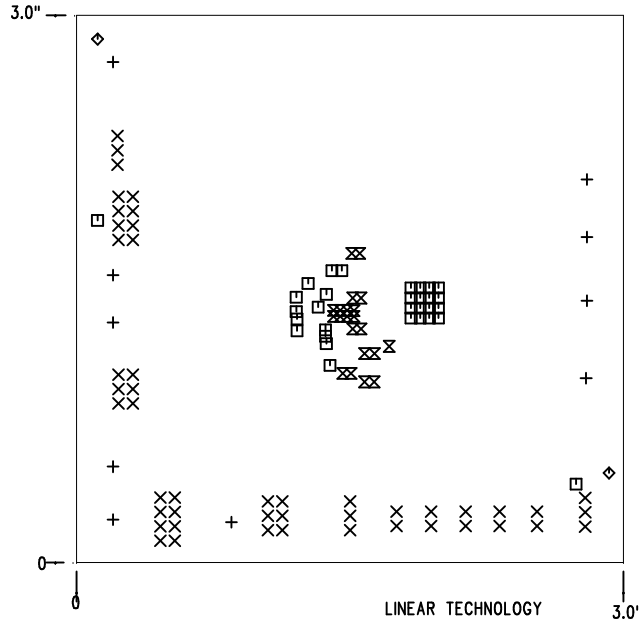
| REVISION HISTORY | | | | |
|---|---|---|---|---|
| ECO | REV | DESCRIPTION | APP. ENG. | DATE |
| – | 1 | PRODUCTION | TOM G. | 03–11–10 |

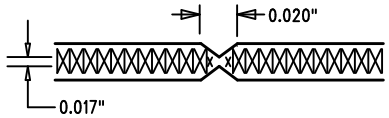



FAB Drawing

LINEAR TECHNOLOGY
DC1467A–LTC3614EUDD
4A MONOLITHIC SYNCHRONOUS
STEP–DOWN REGULATOR
DATE: 03–11–10

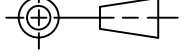
LAYER 1 ( COMPONENT SIDE )
LAYER 2
LAYER 3
LAYER 4
LAYER 5
LAYER 6 (SOLDER SIDE)

1. FAB PER IPC–A–600.
2. MATERIAL: –EPOXY FIBERGLASS, NEMA GRADE FR–4
   –FINISHED THICKNESS TO BE 0.062" +/– .005"
   –TOTAL OF 6 LAYERS WITH 2 OZ. CU ON THE OUTER LAYERS AND 1 OZ. CU ON THE INNER LAYERS.
   –FLAMMABILITY RATING: 94 V–0 MINIMUM.
3. SIZE: CUT TO DIMENSIONS AND TOLERANCES SHOWN. 0.00" ARE PRIMARY DATUMS.
4. DRILLING: –DRILL HOLES PER SCHEDULE. PLATE THROUGH HOLES WITH COPPER, 0.001" THICK MIN.
   –ALL HOLE SIZES ARE SPECIFIED AFTER PLATING.
   –HOLE LOCATION TOLERANCES ARE +/–0.003" IN RELATION TO CENTER
5. FINISH: –SMOBC USING LPI BOTH SIDES, COLOR GREEN.
   –GOLD IMMERSION BOTH SIDES.
   –FOR SILKSCREEN: BOTH SIDES USE WHITE NON–CONDUCTIVE INK.
6. DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE. PAD SIZE CAN BE MODIFIED TO MEET END FINISH.
7. PCBS ARE TO BE RoHS COMPLIANT.
8. SCORING FOR PANELIZED PCB:




| SIZE | QTY | SYM | PLATED | TOL |
|---|---|---|---|---|
| 0.095 | 10 | + | YES | +/–0.003 |
| 0.035 | 47 | X | YES | +/–0.003 |
| 0.01 | 31 | □ | YES | +/–0.003 |
| 0.07 | 2 | ◇ | NO | +/–0.003 |
| 0.02 | 21 | ⧖ | YES | +/–0.003 |

UNLESS OTHERWISE SPECIFIED
DIMENSIONS ARE IN INCHES
TOLERANCES:
0.XX" = ±0.01"
0.XXX" = ±0.005"
INTERPRET DIM AND TOL PER ASME Y14.5M–1994
THIRD ANGLE PROJECTION



| APPROVALS | |
|---|---|
| PCB DES. | AK |
| APP ENG. | TOM G. |
| | |
| | |
| | |

SCALE = NONE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TITLE: FABRICATION DRAWING
4A MONOLITHIC SYNCHRONOUS
STEP–DOWN REGULATOR

| SIZE | IC NO. | REV |
|---|---|---|
| N/A | LTC3614EUDD DEMO CIRCUIT 1467A | 1 |

FILENAME: DC1467A–1.PCB
SHT 1 OF 1